

Electronic Acknowledgement Receipt

EFS ID:	2776802
Application Number:	10542720
International Application Number:	
Confirmation Number:	8410
Title of Invention:	Fast switching power insulated gate semiconductor device
First Named Inventor/Applicant Name:	Barend Visser
Customer Number:	826
Filer:	Tyler Spence/Sarah Simmons
Filer Authorized By:	Tyler Spence
Attorney Docket Number:	050588/295500
Receipt Date:	28-JAN-2008
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Application Type:	U.S. National Stage under 35 USC 371

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Submitted with Payment	yes
Payment Type	Deposit Account
Payment was successfully received in RAM	\$525
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Authorized User	

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File Listing:

Document Number	Document Description	File Name	File Size(Bytes) /Message Digest	Multi Part /.zip	Pages (if appl.)
1	Extension of Time	295500EOT.PDF	48085 b96ec7d7e645d94d05a16d092c7cf9cc5 63351c68	no	1

Warnings:

Information:

2	Miscellaneous Incoming Letter	295500SmallEntityStatement .PDF	101064 fc15af0c7030440c44200534550c0569f 86783266	no	2
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Warnings:

Information:

3	Fee Worksheet (PTO-06)	fee-info.pdf	8172 62b0664815d33310b371127568c059f a450781	no	2
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Warnings:

Information:

Total Files Size (in bytes):	157321
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If a new application is being filed and the application includes the necessary components for a filing date (see 37 CFR 1.53(b)-(d) and MPEP 506), a Filing Receipt (37 CFR 1.54) will be issued in due course and the date shown on this Acknowledgement Receipt will establish the filing date of the application.

National Stage of an International Application under 35 U.S.C. 371

If a timely submission to enter the national stage of an international application is compliant with the conditions of 35 U.S.C. 371 and other applicable requirements a Form PCT/DO/EO/903 indicating acceptance of the application as a national stage submission under 35 U.S.C. 371 will be issued in addition to the Filing Receipt, in due course.

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